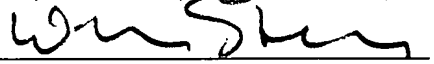


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Docket No.: P2001,0178

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

By: 

Date: August 11, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/661,340 Confirmation No: 8041  
Applicant : Albert Birner et al.  
Filed : September 12, 2003  
Title : Configuration and Method for Making Contact with the Back Surface of a Semiconductor Substrate  
Art Unit : 2818  
Examiner : David Vu  
  
Docket No. : P2001,0178  
Customer No. : 24131

RESPONSE

Hon. Commissioner for Patents

Sir:

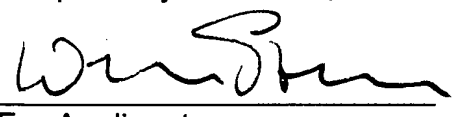
Responsive to the Office Action dated July 27, 2004, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-4, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-4 is solicited.

Respectfully submitted,

  
For Applicants WERNER H. STEMER  
REG. NO. 34,956

Date: August 11, 2004

Lerner and Greenberg, P.A.  
Post Office Box 2480  
Hollywood, FL 33022-2480  
Tel: (954) 925-1100  
Fax: (954) 925-1101  
/bmb